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CROSS CURRENT CONTROL FOR POWER CONVERTER SYSTEMS AND INTEGRATED MAGNETIC CHOKE ASSEMBLY

Background of Invention

[0001] Paralleling multiple power converters is a common practice in the telecom and UPS (uninterruptible power supply) industries to increase overall system power capacities and to enhance system reliabilities by building redundancy. Typical examples of such power converters are single phase or three phase converters comprising inverters, rectifiers and DC/DC converters. Typically all the parallel power converters are gated synchronously and are tied together through isolation transformers to limit the cross current. Synchronous gating implies that the gate controls for the parallel converters are perfectly aligned.

[0002] Another way to operate the parallel power converters is through interleaved gating. Interleaved gating means that the switching patterns of the parallel converters are uniformly phase shifted, rather than synchronized. Interleaved gating has several advantages such as having reduced harmonic filter size, increased system efficiency, greatly enhanced control bandwidth (and thus improved dynamic performance), and potentially reduced EMI (electromagnetic interference).

[0003] Common mode current that circulates among the paralleled multiple converters or within paralleled converter systems that does not contribute to the output to the load is typically referred to as "cross current." Both synchronous and interleaved gating control embodiments typically result in undesirable cross current with the cross current being more severe in interleaved embodiments. In ideal conditions

synchronous gating does not lead to cross current, but in actual circuits using synchronous gating cross current exists due to mismatched circuit parameters. One way to reduce the cross current is by using an isolation transformer. In embodiments with isolation transformers, these isolation transformers account for almost one third of the system cost.

[0004] The existing techniques for controlling cross current without using an isolation transformer all suffer from certain inherent disadvantages. For example, using current balancers or inter-phase reactors for controlling cross current requires design of an inter-phase reactor. Such design cannot be standardized for arbitrary numbers of converters in parallel.

[0005] Another technique of controlling cross current without using isolation transformers is through use of "combined-mode" current control by treating two parallel converters as one converter, selecting the "optimum" switching vector, and adding a current balancer. The "combined-mode" approach is not suitable for more than two converters in parallel because the modulator complication level increases drastically when dealing with more than two parallel converters.

[0006] It would therefore be desirable to have an improved cross control system for interleaved or synchronous operation of multiple power converters, arranged in parallel, without using isolation transformers.

Summary of Invention

[0007] Briefly, in accordance with one embodiment of the present invention, a cross current control system for multiple, parallel-coupled power converters comprises common mode chokes, local cross current detectors, local cross current feedback controllers and local converter controllers. Each of the common mode chokes is coupled to a respective power converter. Each local cross current detector is configured for obtaining common mode cross currents from a respective output line of a respective power converter. Each of the local cross current feedback controllers is configured for receiving the common mode cross currents from respective local cross current detectors, calculating a resultant cross current, and generating a local feedback control signal. Each of the local converter controllers is configured for using

a respective local feedback control signal to drive the respective power converter in accordance with a coordinated switching pattern which may comprise either an interleaved or a synchronous switching pattern with respect to the other power converters.

[0008] In accordance with another embodiment of the invention, a method of controlling cross-current through multiple, parallel-coupled power converters comprises providing common mode chokes, each coupled to a respective power converter; and obtaining common mode cross currents from output lines of the power converters. The method further comprises for each respective power converter, calculating a resultant cross current by using the respective common mode cross currents, generating a local feedback control signal by using the resultant cross current, and driving the respective power converter by using the respective local feedback control signal in accordance with a coordinated switching pattern with respect to the other power converters.

[0009] In accordance with another embodiment of the invention, an integral choke assembly comprises a common mode choke and a differential mode choke. The common mode choke comprises a common mode core wound with at least two common mode coils and a differential mode choke comprises a differential mode core wound with at least one differential mode coil. The common and differential mode choke cores are configured so that at least one magnetic path is shared by magnetic flux generated by common and differential mode coils.

Brief Description of Drawings

[0010] These and other features, aspects, and advantages of the present invention will become better understood when the following detailed description is read with reference to the accompanying drawings in which like characters represent like parts throughout the drawings, wherein:

[0011] FIG. 1 illustrates a cross current control system according to one embodiment of the invention;

[0012] FIG. 2 illustrates a cross current control system for an individual converter according to one embodiment of the invention;

- [0013] FIG. 3 illustrates a DC link common mode choke with a common DC bus;
- [0014] FIG. 4 illustrates an AC link common mode choke with a common DC bus;
- [0015] FIG. 5 illustrates a DC link common mode choke with separate DC bus;
- [0016] FIG. 6 illustrates an AC link common mode choke with separate DC bus;
- [0017] FIG. 7 illustrates an AC link choke comprising an integrated magnetic choke according to one embodiment of the invention;
- [0018] FIG. 8 illustrates an integrated magnetic structure showing a closed rectangular and an E core according to one embodiment of the invention and magnetic flux paths generated by coils shown in Fig. 9;
- [0019] FIG. 9 illustrates the embodiment of FIG. 8 with three common mode coils wound around the closed rectangular core and a respective differential mode coil on each leg of the E core;
- [0020] FIG. 10 illustrates an embodiment of the integrated magnetic structure having a top closed rectangular core, a bottom closed rectangular core and three posts;
- [0021] FIG. 11 illustrates the embodiment of FIG. 10 with the top closed rectangular core wound with three common mode top coils, the bottom closed rectangular core wound with three common mode bottom coils and three posts with a respective differential mode coil on each post, and magnetic flux paths generated by coils shown in Fig. 12;
- [0022] FIG. 12 illustrates an embodiment of the integrated magnetic choke comprising a single phase common mode choke and a single phase differential mode choke;
- [0023] FIG. 13 illustrates an embodiment of an integrated magnetic structure showing a closed rectangular core and a U core, and magnetic flux paths generated by coils shown in Fig. 14;
- [0024] FIG. 14 illustrates an embodiment of FIG. 13 with the top closed rectangular core wound with two common mode coils and a U core wound with two differential mode coils on each leg;
- [0025] FIG. 15 illustrates an embodiment of an integrated magnetic structure having a

top closed rectangular core, a bottom closed rectangular core and two posts, and magnetic flux paths generated by coils shown in Fig. 16; and

[0026] FIG. 16 illustrates an embodiment of FIG. 15 with the top and bottom closed rectangular cores wound with common mode top coils and a common mode bottom coils respectively, and the two posts with a differential mode coil on each post.

Detailed Description

[0027] One embodiment of the present invention is a cross current control system 1 comprising a multi-converter system 10 (or multiple multi-converter systems 10), as illustrated in Fig 1, to limit the cross current among multiple, parallel, power converters 20, operating in a coordinated fashion without an isolation transformer to drive the load 500. The cross current control system comprises at least two power converters and their respective controls which are shown as individual power converter systems 22, 222, and 322 for purposes of example and each comprise a common mode choke 60, local cross current detectors 70, a local cross current feedback controller 80 and a local converter controller 90, and one or more converters 20.

[0028] The common mode choke is particularly useful for reducing the cross current at the switching frequency level caused by asynchronous switching patterns (created by interleaved control embodiments or imperfect synchronous control embodiments) applied to each of the parallel power converters. Each of the common mode chokes is coupled to a respective power converter. The local cross current detectors obtain common mode cross currents from output lines of respective power converters and feed them into a summer 72 which outputs the summed common mode current 24 (total cross current through the individual power converter). The local cross current feedback controllers receive the common mode cross currents from respective local cross current detectors (either directly or through the summer), calculate a resultant cross current, and generate a local feedback control signal 34. Each of the local converter controllers uses a respective local feedback control signal to drive the cross current of the respective power converter towards zero in accordance with a coordinated (interleaved or synchronized) switching pattern with respect to the other power converters. The local converter controller can be implemented by using a

proportional regulator, an integral regulator, or a proportional–integral regulator for driving a respective cross current to zero. The bandwidth of local converter controller is limited by the switching frequency of the respective power converter.

[0029] The local cross current feedback controller, as discussed above, nullifies lower than switching frequency cross–current due to imperfectly matched circuit parameters, such as filter parameters, power switches voltage drop, or gate driver dead–time.

[0030] In one embodiment, the cross current control system further comprises modulators 100, each of which receives a local converter controller signal from a respective local converter controller and generates a firing signal for driving a respective power converter. The modulator translates a continuous signal from the local converter controller into a switching signal for driving the power converter.

[0031] In a more specific embodiment, the cross current control system of Fig. 1 further comprises a global feedforward controller 50, which detects switching signals (patterns) 28 of the power converters and generates counter balance zero–sequence global feedforward control signals 30. Each of the local converter controllers further uses a respective global feedforward control signal 30 (in addition to local feedback control signal 34) to drive the respective power converter. Global feedforward controller 50 takes the information from all the parallel converters in a single converter system and derives a global feedforward control signal for each individual power converter.

[0032] In another more specific embodiment, which can be used in conjunction or separately from the global feedforward controller embodiment, the cross current control system of Fig 1 further comprises a global feedback controller 40, which receives the common mode cross currents from each of the power converters (and the summed total cross current 26 across multiple power converters), calculates a resulting global cross current, and generates global feedback control signals 32. Each of the local converter controllers further uses a respective global feedback control signal to drive the respective power converter (in addition to local feedback control signal 34 and optionally in addition to global feedforward control signal 30).

[0033] The global feedforward controller is designed to eliminate the lower frequency cross currents flowing within one multi-converter system 10, while the global feedback controller is used to control the cross current flowing out from one multi-converter system 10 to other multi-converter systems 10 (i.e. referring to Fig 1, cross current through power converter systems 10). In an embodiment having a plurality of multi-converter systems 10 requiring fast cross current control, all the three controllers (local cross current feedback controller, global feedforward controller and global feedback controller) are particularly useful.

[0034] Fig 2 illustrates a specific embodiment of the invention for an individual power converter system 22. Individual power converter system 22 comprises a common mode choke 60 and local cross current feedback controller 80 for controlling the cross current. The functions of other elements such as local cross current detectors 70, summer 72, local converter controller 90, and modulator 100 are same as described above with respect to Fig 1.

[0035] The cross current control system discussed above applies both to single-phase and three-phase multiple power converters. The parallel converters may be rectifiers, inverters, or DC/DC converters or their combinations for UPS (uninterruptible power supply) or any other power conditioning systems.

[0036] Figs 3-6 illustrate various embodiments of a power converter system. Although the embodiments shown are for double conversion (AC to DC and DC to AC), they are equally applicable to other converter topologies. Inductor 38 reduces the current ripples, and capacitor 36 smoothes DC link voltage generated during the switching operation of the power converters. Common mode chokes are illustrated as DC link choke 110 in Figs. 3 and 5 and AC link choke 120 in Figs. 4 and 6. In the embodiment of the present invention as shown in Fig. 3 and Fig. 4, the power converters share a common DC bus 130. In another embodiment of the present invention as shown in Fig. 5 and Fig. 6, the power converters comprise separate DC busses 130.

[0037] In another embodiment of the present invention, the AC link choke comprises a discrete magnetic choke 120 as shown in Fig. 4 and Fig. 6.

[0038] In another embodiment of the present invention the AC link choke comprises an

[0040] In another embodiment of the integrated magnetic structure as shown in Fig. 10 and Fig. 11, the common mode core comprises a top closed rectangular core 190 wound with three common mode top coils 142 and a bottom closed rectangular core 200 wound with three common mode bottom coils 143. The differential mode core comprises three posts 210 as shown in Fig. 11, with a respective differential mode coil 144 on each post as shown in Fig. 12. The three posts are arranged between the top and bottom closed rectangular cores and have a magnetic flux path 178. The posts share a part of top and bottom rectangular magnetic flux paths 146 and 147. The integrated magnetic structure of Fig. 11 results in higher common mode inductance than the integrated magnetic structure of Fig. 9.

[0041] In accordance with another embodiment of the invention, which is particularly useful in single phase choke embodiments and which is described below for several specific examples, an integral choke assembly comprises a common mode choke and a differential mode choke. The common mode choke comprises a common mode core

wound with at least two common mode coils and a differential mode choke comprises a differential mode core wound with at least one differential mode coil. The common and differential mode choke cores are configured so that at least one magnetic flux path is shared by magnetic flux generated by common and differential mode coils.

[0042] An another embodiment of the integrated magnetic choke for single phase or DC/DC converters as shown in Fig. 12, comprises an integrated magnetic structure coupling a single phase common mode choke 162 and a single phase differential mode choke 164. The single phase common mode choke and single phase differential mode chokes are connected in series. In one embodiment, the integrated magnetic structure comprises a common mode core and a differential mode core with the common mode core comprising a higher permeability material than the differential mode core.

[0043] In one example, as shown in Fig. 13 and Fig. 14, the common mode core comprises a closed rectangular core 166 wound with two common mode coils 142 and the differential mode core comprises a U core 168 wound with two differential mode coils 144 on each leg. The legs of U core face the closed rectangular core and have a magnetic flux path 169. The legs share a part of magnetic flux path 145 of the closed rectangular core.

[0044] In another example, as shown in Fig. 15 and Fig. 16, the common mode core comprises a top closed rectangular core 172 wound with common mode top coils 142 and a bottom closed rectangular core 174, also wound with common mode bottom coils 143. The differential mode core comprises two posts 176 with a differential mode coil 144 on each post. The two posts are arranged between the top and bottom closed rectangular cores and have a magnetic flux path 178. The posts share a part of the magnetic flux paths 146 and 147 of the top and bottom closed rectangular cores.

[0045] The various embodiments of integrated chokes 220 discussed above are useful in combination with cross current control systems as discussed above and can be useful in other embodiments as well. For example, integrated choke embodiments are useful for EMI filtering in DC/DC converters.

[0046] While only certain features of the invention have been illustrated and described

[illegible]